The documentation and process
|conversion measures necessary to
|comply with this amendment shall
|be completed by 12 December 1992

INCH-POUND

MIL-S-19500/569
AMENDMENT 3
12 June 1992
SUPERSEDING
AMENDMENT 2
8 September 1989

MILITARY SPECIFICATION

SEMICONDUCTOR DEVICE, FIELD EFFECT TRANSISTOR, N-CHANNEL, SILICON TYPES 2N6966, 2N6967, 2N6968, and 2N6969

JANTX, JANTXV, AND JANS

This amendment forms a part of MIL-S-19500/569, dated 15 September 1987, and is approved for use by all Departments and Agencies of the Department of Defense.

PAGE 1

1.3, maximum ratings, I_{D1} column: Delete "mA" and substitute "A".

PAGE 3

- 4.1, Screen 12, JANS column, after condition A: Add ";", after see (4.3.1): Add "condition C shall precede condition A".
- 4.3.1, second line: Delete "-0°C," and substitute "-5°C,"

PAGE 4

4.5.3, delete the main text only and substitute the following:

"Thermal response (ΔV_{SD} measurements). The delta V_{SD} measurements shall be performed in accordance with method 3161 of MIL-STD-750. The delta V_{SD} conditions (I_H and V_H) and maximum V_{SD} limit shall be derived by each vendor from the thermal response curves (see figure 2). The chosen ΔV_{SD} measurement and conditions for each device in the qualification lot shall be submitted in the qualification report. The chosen ΔV_{SD} shall be considered final after the manufacturer has had the opportunity to test five consecutive lots."

PAGE 11

TABLE I, subgroup 2, gate current, conditions column: Delete " V_{GS} = ± 20 V dc," and substitute " V_{GS} = ± 20 V dc,".

TABLE I, subgroup 2, static drain to source "on"-state resistance, condition column: Delete " I_D = 10 A dc, I_D = 7 A dc, I_D = 6.2 A dc, I_D = 3.3 A dc" and substitute " I_D = rated I_{D2} (see 1.3)".

*The attached insertable replacement pages listed below are replacements for stipulated pages. When the new pages have been entered in the document, insert the amendment as the cover sheet to the specification.

 Replacement page
 Page replaced

 13
 13

 14
 14

AMSC N/A

1 of 3

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

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PAGE 12

TABLE I, subgroup 2, forward transconductance, conditions column: Delete "Pulsed (see 4.5.1)" and substitute " I_D = Rated I_{D2} (see 1.3), Pulsed (see 4.5.1)".

TABLE I, subgroup 3, gate current, conditions column: Delete " V_{GS} = ± 20 V dc" and substitute " V_{GS} = ± 20 V dc and ± 20 V dc".

*TABLE I, subgroup 4, delete in its entirety and substitute the following:

Inspection	MIL-STD-750		LTPD 1/					1
	Method	Conditions	JANS	JANTX,	Symbol	Hin	Max	Unit
Subgroup 4								
Switching time test	3472	I _D = Rated I _{D2} (see 1.3)	 				! 	
	į	V _{GS} = 10 V dc Gate drive	j			j	j J	j
	İ	Impedance = 7.50Ω	j I			İ	<u> </u>	İ

PAGE 16

TABLE IIa, subgroup 4, intermittent operation life, conditions column: Delete "see 4.3.1".

TABLE IIa, subgroup 4, intermittent operation life, method column: Delete "1037" and substitute "1042".

TABLE IIa, subgroup 5, accelerated steady state operation life, conditions column: Delete " T_J = 200°C", and substitute "(except) T_J = +200°C, +20°C, -0°C".

PAGE 18

TABLE IIb, subgroup 3, intermittent operation life, conditions column: Delete "A" and substitute "The heating".

PAGE 19

TABLE III, subgroup 2, terminal strength (lead torque), conditions column: Delete "6 in oz." and substitute "3 in-oz".

TABLE III, subgroup 2: Delete "Visual and mechanical evaluation, method 2071".

PAGE 20

TABLE III, subgroup 6, intermittent operation life, conditions column: Delete "A" and substitute "The heating".

PAGE 21

TABLE IV, step 3, gate current, conditions column: Delete " $V_{GS} = \pm 20$ V dc,", and substitute " $V_{GS} = \pm 20$ and -20 V dc,".

TABLE IV, step 5, static drain to source on-state resistance, conditions column: Delete " I_n = 10 A dc, I_D = 7 A dc, I_D = 6.2 A dc, I_D = 3.3 A dc" and substitute " I_D = rated I_{D2} , (see 1.3)".

TABLE IV, step 6, drain to source on-state voltage, maximum limits column: Delete

1.26 3.41 | 2.81 | "

and substitute

" + 1.7 | 2.4 4.13 5.1 | "

*TABLE IV, step 8, min/max column: Delete footnote "1/."

TABLE IV, delete footnotes 1/ and 2/ in their entirety.

The margins of this amendment are marked with an asterisk to indicate where changes (additions, modifications, corrections, deletions) from the previous amendment were made. This was done as a convenience only and the Government assumes no liability whatsoever for any inaccuracies in these notations. Bidders and contractors are cautioned to evaluate the requirements of this document based on the entire content irrespective of the marginal notations and relationship to the last previous amendment.

CONCLUDING MATERIAL

Custodians:

Army - ER Navy - EC

Air Force - 17

NASA - NA

Review activities:

Air force - 11, 70, 80

Navy - TD

* NASA - LRC, MSF

User activity:

Air Force - 19

Preparing activity:

Navy - EC

Agent:

DLA - ES

(Project 5961-1311)

TABLE I. Group A inspection - Continued.

Inspection	MIL-STD-750		<u> </u>	TPD 1/	<u> </u>	<u> </u>		<u> </u>
	Method	Conditions	JANS	JANTX, JANTXV	Symbol	Hin	 Max	 Unit
Subgroup 4 - Continued	 	 	 		 			
Turn-on delay time		1			l td(on)		!	
2N6966	:	V _{DD} = 34 V dc		1	 	İ	30	ns
2N6967	j	iv' = 75 v dc	İ	1	!		30	ns
2N6968	i	V ₀₀ = 175 V dc	İ	!	1	į	35	ns
2N6969	ĺ	V _{DD} = 175 V dc V _{DD} = 200 V dc	i	i	Ì	İ	35	ns
•	ĺ	00	Ì	1	, 	1	1	1
Rise time	1		1	i !	tr		 	
2N6966	!	 V _{DD} = 34 V dc		i		i	60	ns
2N6967	!	V = 75 V dc		1			60	ns
2N6968) I	V = 175 V dc			İ	:	30	ns
2N6969	i	V _{DD} = 75 V dc V _{DD} = 175 V dc V _{DD} = 200 V dc				i	30	ns
Turn-off delay time	 		i 	!	^t d(off)			<u> </u>
20/0//	!	7/ 1/ -	İ	!		1 1	80	!
2N6966	 	V _{DD} = 34 V dc	1	1		! !	80	ns
2N6967 2N6968	!	VDD = 75 V dc	ļ	:		1 1	90	ns
2N6969	!	V _{DD} = 75 V dc V _{DD} = 175 V dc V _{DD} = 200 V dc	1	,		1 1	90	ns
	1	1 DD = 200 v dc		;			/0	
Fall time			!		t _f			
2N6966		V _{DD} = 34 V dc		i			30	ns
2N6967		V-= 75 V dc	i	i i		i i	60	ns
286968		V _{DD} = 75 V dc V _{DD} = 175 V dc	i	i i		i i	35	ns
2N6969		V _{DD} = 200 V dc	į	į į		į į	30	ns
Subgroup 5								
Safe operating area test		See figure 3, $V_{DS} = 80\%$ of rated V_{DS} and $V_{DS} \leq 200$ V max						
ligh voltage dc SOA		t _p = 1 s		;				
lectrical	į	Con table IV	i			i		
lectrical		See table IV,	!	1 1		!!!	ŀ	
measurements		steps 1, 2, 3, 4,	I	1			!	
i	!	5, 6, and 7	1	1 1		1 1	i	
Subgroup 6	1			; 			1	
Story out o	i		1			i i	,	
ot applicable	j		1	i i		ı İ	- 1	

See footnote at end of table.

Supersedes page 13 of MIL-S-19500/569 of 15 September 1987

TABLE I. Group A inspection - Continued.

	1					Ţ	!	Ţ	
	<u> </u>	MIL-STD-750	<u> LT</u>	PD 1/	 		- 	<u> </u>	
Inspection	 Hethod	Conditions	JANS	JANTX,	Symbol	Min	Max	Unit	
Subgroup 7	 	 					! 	 	
 Gate charge 	 3471 			1	· .		 	1	
Test no. 1	! !				ļ		-		
On-state gate charge					a _{g(on)}		-	nC	
 2N6966 2N6967 2N6968 2N6969	 		! ! !		 	30 30 30 30	77 77 77	 	
Test no. 2			! !						
 Gate to source charge	 		 		Q _{gs}		1	nC	
2n6966 2n6967 2n6968 2n6969] 	 		4.6	18 18 18 18	1	
Test no. 3								[
Gate to drain charge					^Q gd	 	<u>}</u> 	nC 	
2N6966 2N6967 2N6968 2N6969	 					13 13 13 13 13	40 40 40 40		

See footnote at end of table.